

10445680
01/10/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10043680	01/10/2002	365	222	2824	VAN THI TU NGUYEN

APPLICANTS: Cowles Timothy; Shore Michael; Mullarkey Patrick;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/803,365 03/08/2001

US 6449203 issued
Y/ VTN 9/10/02

** FOREIGN APPLICATIONS VERIFIED:

N/ VTN

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	500792.03
Verified and Acknowledged Examiner's initials VTN			
TITLE : Refresh controller and address remapping circuit and method for dual mode full/reduced density DRAMs			

11/4/03 Formal Drawings (4 sheets) set 11/10/02

DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for OGI
4	5	1	7	1
NOTICE OF ALLOWANCE MAILED				
10-14-03				
ISSUE FEE				
Amount Due		Date Paid		
30		\$		
ISSUE BATCH NUMBER				
DRAFTING				

SANTHU NGUYEN
PATENT EXAMINER
ISSUE FEE IN FILE
Van Thu Nguyen 10/10/03
(Primary Examiner) (Date)

(Legal Instruments Examiner) (Date)

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